

Article

Advanced Distributed Cooperative Secondary Control of Islanded DC Microgrids

Anuluwapo Aluko ^{*}, Elutunji Buraimoh , Oluwafemi Emmanuel Oni  and Innocent Ewean Davidson 

Department of Electrical Power Engineering, Durban University of Technology, Durban 4001, South Africa; elutunji@dut.ac.za (E.B.); oluwafemio1@dut.ac.za (O.E.O.); innocentd@dut.ac.za (I.E.D.)

* Correspondence: 21752070@dut4life.ac.za

Abstract: In an islanded DC microgrid with multiple distributed generators (DGs), the droop control is employed to realize proportional current sharing among the DGs in the microgrid. The action of the droop control causes a deviation in the DC bus voltage which is exacerbated by the line impedance between the DG and the DC bus. In this paper, an advanced distributed secondary control scheme is proposed to simultaneously achieve accurate voltage regulation and cooperative current sharing in the islanded DC microgrid system. The proposed distributed secondary controller is introduced in the cyber layer of the system, and each controller shares information with neighbouring controllers via a communication network. The distributed technique maintains the reliability of the overall system if some part of the communication link fails. The proposed controller uses the type-II fuzzy logic scheme to adaptively select the secondary control parameters for an improved response of the controller. The sufficient conditions to guarantee the stability of the proposed controller are derived using the Lyapunov method. Comprehensive tests under different operating scenarios are conducted to demonstrate the robustness of the proposed control scheme.

Keywords: DC microgrid; distributed control; distributed generation; fuzzy logic system; power sharing; secondary control; voltage control



Citation: Aluko, A.; Buraimoh, E.; Oni, O.E.; Davidson, I.E. Advanced Distributed Cooperative Secondary Control of Islanded DC Microgrids. *Energies* **2022**, *15*, 3988. <https://doi.org/10.3390/en15113988>

Academic Editor: Adel Merabet

Received: 18 April 2022

Accepted: 19 May 2022

Published: 28 May 2022

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1. Introduction

Owing to the growing environmental concerns and continuous depletion of fossil fuel reserves, the adoption of renewable energy systems (RESs) and large-scale energy storage systems (ESSs) have been on the rise. However, the integration of these sources gives rise to various operational and technical concerns such as congestion of transmission lines, unstable voltage profiles, and reduced reserves for frequency regulation [1]. To achieve a more stable control and flexible operation, the concept of a microgrid has been introduced in the last few decades. Microgrids are small-scale power grids that consist of distributed energy resources (DERs) or distributed generators (DGs), electric vehicles (EVs), and domestic and commercial loads [2]. They expedite the use of RESs and have the advantages of reduced losses and proximity to end-users. Microgrids can be operated in islanded or grid-connected modes, and they can have alternating current (AC), direct current (DC), or hybrid AC-DC bus configurations. In the past decade, the ubiquitous use of DC sources such as solar photovoltaic (PV) systems, battery energy storage systems (BESS), supercapacitor energy storage systems, fuel cells, and DC loads, DC microgrids have gained appreciable attention [3]. Compared to the traditional AC microgrids, DC microgrids do not have the problems of synchronization using the phase-locked loop (PLL), frequency control, reactive power control, harmonics, inrush currents, and power factor losses [4,5]. Therefore, studies have shown that DC microgrids have about 15% more efficiency when compared to AC microgrids [6].

In the DC microgrid system, the DGs are connected to the DC bus via a controlled DC/DC power electronic converter interface. The DC bus voltage regulation is the major

control objective. When multiple DGs or ESSs are connected in parallel to a common DC bus, the droop control is used to achieve power sharing in the DGs and ESSs without communication links [7]. The droop control is implemented by adding a virtual (droop) resistance loop to the primary control loop of the DC/DC converter. The primary control loop consists of the proportional-integral (PI)-based current and voltage controllers that generate the voltage reference for the pulse width modulation (PWM) that switches the converter [8]. Despite the simple implementation of droop control, it has several limitations that inhibit its application. The most notable drawbacks are the voltage deviation and the current-sharing error caused by the propagation of the voltage deviation. Therefore, a higher level of control (secondary control) is required to solve the voltage deviation and current-sharing problems caused by the droop control method [9]. The secondary control may be centralized, decentralized, or distributed. The conventional approach is the centralized secondary in which the error measurement is fed into a central controller, usually a PI-type controller, and the output of the controller is sent to the active DGs via communication links [10]. The voltage deviation and current-sharing error are the inputs to the central controller, and the output (adjustment signal) of the secondary controller enhances the voltage regulation, average power sharing, reliability, and power quality of the islanded DC microgrid. In [11], a coordinated centralized controller based on DC bus signaling was proposed for DC bus voltage regulation and maintaining the state-of-charge (SOC) of the ESS in the microgrid. In [12], the linear-quadratic Gaussian (LQG) and synergistic control strategies were used to design a centralized controller to stabilize a medium-voltage DC microgrid during large-signal disturbance(s). The drawbacks of the centralized secondary control are less flexibility and scalability, susceptibility to single-point failure, and less reliability. In the decentralized secondary control scheme, the sources in the microgrids are locally controlled. The local measurements are used as input signals to the controller to generate an appropriate adjustment signal. In [13], a decentralized droop-based control scheme was proposed to ensure voltage regulation, energy management, and SOC recovery in an islanded DC microgrid. In [14], a decentralized robust backstepping voltage controller was proposed for voltage regulation of a DC microgrid. In [15], a globally decentralized secondary control scheme was proposed for power sharing for DC loads. In [16], an adaptive coordination control strategy was proposed for the secondary control of a DC microgrid. The proposed decentralized control improved the life cycle of the battery ESS. While the decentralized secondary control is easy to implement, it has the limitation of deteriorating the overall performance of the DC microgrid because of the absence of global communication; however, the output of the controller may be sufficient to compensate for the required deviation caused by the droop control.

The distributed secondary control combines the advantages of centralized and decentralized secondary controls. In the distributed secondary control, each DG unit has its secondary controller and shares (receives) information such as voltage, output current, etc, with neighbouring secondary controllers of other DG units. The advantage of this approach is the ability to retain functionality if some part of the communication link fails; thus, there is a reduced risk of single-point failure [17]. In [18], a distributed nonlinear sliding mode control scheme was proposed for power balance in a DC microgrid considering variable power generation. In [19], a distributed cooperative control using consensus protocol was proposed to maintain the voltage profile of an islanded DC microgrid with hybrid ESS considering variable RES generation. In [20], one secondary controller was dedicated to voltage restoration, and other distributed secondary controllers were proposed to achieve proportional power allocation among the DGs in the DC microgrid. In [21], a consensus-based distributed secondary control scheme was proposed for voltage recovery and current sharing in a DC microgrid. In the proposed scheme, two PI controllers are employed for independent voltage recovery and current sharing, and the outputs of the controllers are sent to the local controllers as adjustment signals. In [22], the integral-type distributed secondary control scheme was proposed for voltage restoration and current sharing in an islanded DC microgrid.

In the aforementioned reports, the proposed schemes are based on conventional control schemes. Fuzzy logic systems have been identified as an alternative approach to provide a more robust solution to control problems in the emerging power grids such as DC microgrids [23]. The type-I fuzzy controllers are the most common type of fuzzy controllers that are easily designed with little knowledge of the system model, and they have been successfully applied in various power system problems [24,25]. However, the major limitation of the type-I fuzzy logic systems is that they are not robust enough to handle uncertainties that are associated with dynamic environments such as DC microgrids [26]. Thus, type-II fuzzy logic systems have been proposed as an efficient control scheme to overcome these uncertainties and external disturbances because they add an extra degree of freedom in the output that is provided by the footprint of uncertainty [27].

In this paper, an advanced distributed secondary control scheme is proposed for voltage regulation and average current sharing for an islanded DC microgrid. The proposed controller is based on the type-II fuzzy logic control; the inputs to the fuzzy system are the voltage deviation and the current-sharing error. The Lyapunov-based method is used to analyze the stability of the proposed controller. The theoretical and experimental analysis shows the robustness of the proposed secondary controller to achieve the control objective in the DC microgrid.

The remainder of this paper is organized as follows: Section 2 presents the modeling of the DC microgrid; Section 3 presents the distributed secondary control scheme; Section 4 presents the proposed type-II fuzzy-based distributed secondary control for voltage regulation and current sharing in the DC microgrid; Section 5 discusses the results of the simulation; Section 6 highlights the conclusion of the paper.

2. Basics of DC Microgrid

In this section, the preliminaries of the islanded DC microgrid is presented. The power flow and communication flow models are modeled, and the control objectives of the study system are presented.

2.1. Power Flow

The generalized physical model of an islanded DC microgrid with multiple DGs is depicted in Figure 1. Considering an islanded DC microgrid with N number of DGs connected to a common DC bus, the DGs can be represented by its Thevenin's equivalent connected to the DC bus via the line impedance. The power flow model of the i^{th} DG can be mathematically expressed as

$$\begin{cases} L_i \frac{dI_{L_i}}{dt} = d_i \cdot V_{s_i} - V_i, \\ C_i \frac{dV_i}{dt} = I_{L_i} - I_{o_i}, \\ V_{dc} = V_i - R_{line_i} I_{o_i}, \end{cases} \quad (1)$$

where L is the filter inductance, C is the filter capacitance, d is the duty cycle of the converter, V_s is the source voltage of the DG, I_L is the filter inductor current, V is the output voltage of the DG, I_o is the output current of the DG, R_{line} is the line impedance between the i^{th} DG and the DC bus, and V_{dc} is the voltage at the DC bus. It is worth noting that the line impedance is neglected due to the presence of the LC filter that makes its effect negligible. For the DC microgrid with N DGs, the power flow equation in (1) is rewritten as

$$\begin{cases} \mathbf{L} \frac{d\mathbf{I}_L}{dt} = \mathbf{d} \cdot \mathbf{V}_s - \mathbf{V}, \\ \mathbf{C} \frac{d\mathbf{V}}{dt} = \mathbf{I}_L - \mathbf{I}_o, \\ V_{dc} = \mathbf{V} - \mathbf{R}_{line} \mathbf{I}_o, \end{cases} \quad (2)$$

where $\mathbf{L} = \text{diag}\{L_i\}$, $\mathbf{C} = \text{diag}\{C_i\}$, $\mathbf{V}_s = \text{diag}\{V_{s_i}\}$, $\mathbf{R}_{line} = \text{diag}\{R_{line_i}\}$, $\mathbf{V} = [V_1, V_2, \dots, V_N]^T$, $\mathbf{I}_o = [I_{o_1}, I_{o_2}, \dots, I_{o_N}]^T$, $\mathbf{I}_L = [I_{L_1}, I_{L_2}, \dots, I_{L_N}]^T$, and $\mathbf{d} = [d_1, d_2, \dots, d_N]^T$.

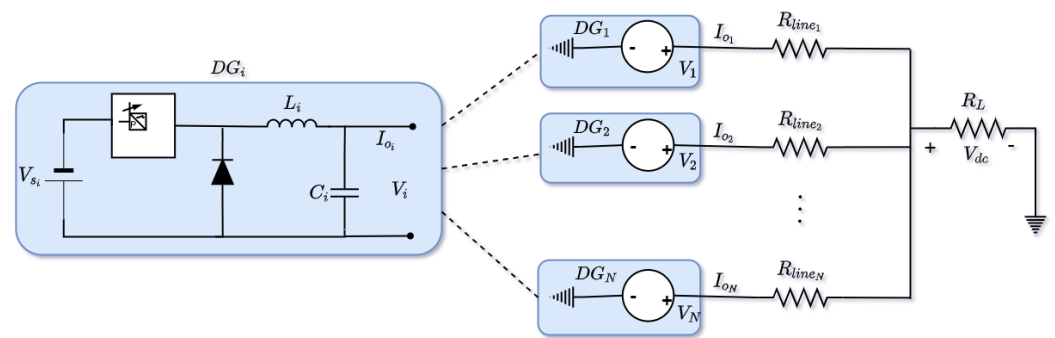


Figure 1. Power system model of a DC microgrid.

2.2. Information Flow

In an islanded DC microgrid, the communication network ensures the timely exchange of information between the DG controllers. The flow of information is based on the topology of the communication system, usually a sparse communication network. The communication network can be mathematically represented using graph theory as

$$\mathcal{G} = \{\mathcal{V}, \mathcal{E}, \mathcal{A}\}, \tag{3}$$

where \mathcal{V} is the set of vertices (DGs), \mathcal{E} is the set of edges (communication link) between the DGs, and \mathcal{A} is the adjacency matrix, $\mathcal{A} = [a_{ij}]$, that represents the communication weights between the DGs.

2.3. Problem Formulation and Control Objectives

In the islanded DC microgrid with more than one DG connected to a common bus, the droop control is traditionally used to achieve proportional power sharing. The droop control is embedded in the primary control loop (current and voltage PI-based controllers) of the DC/DC converter, thus acting in a decentralized way. For DG_i , the voltage reference signal for current sharing based on the droop controller can be expressed as

$$V_{ref_i} = V_{ref} - R_d I_{o_i}, \tag{4}$$

where V_{ref} is the reference(nominal) voltage of the DC bus, and R_d is the droop resistance. Traditionally, the output voltage of the DG should track the reference voltage as

$$V_{ref_i} = V_{ref} \tag{5}$$

By combining (2)–(5), we obtain

$$V_{dc} = V_{ref} - (R_{line_i} + R_d) I_{o_i} \tag{6}$$

From (6), it can be inferred that the DC bus voltage, V_{dc} , will always be unequal to the reference voltage, V_{ref} , because the output current, I_o , will not be equal to zero. Similarly, the magnitude of the deviation is dependent on the value of the droop resistance and line impedance. Large values of the droop resistance will increase the DC bus voltage deviations, and the magnitude of the line impacts the bus voltage.

In this work, we consider the islanded DC microgrid with N DGs that should achieve average power sharing to meet the load. For a constant DC voltage, the output current-sharing ratio of DG_i and DG_j can be expressed as

$$\frac{k_i I_{o_i}}{k_j I_{o_j}} = \frac{R_{line_j} + R_{d_j}}{R_{line_i} + R_{d_i}} \tag{7}$$

where k_i and k_j are the current-sharing ratio of DG_i and DG_j . It is important to mention that with the current-sharing ratio, each DG may be prespecified at the design stage, usually dependent on the capacity of the DG. Considering the ESS-based DG system used in this work, the current-sharing ratio of the DGs are constant values that are prespecified. To ensure simultaneous current sharing and maintain the DC bus voltage at reference value, the control objectives of the DC microgrid are formulated as follows:

Objective 1: To achieve global voltage regulation at the DC bus, the voltage deviation must be equal to zero. This is mathematically expressed as

$$\begin{aligned}\Delta V_{dc} &= V_{dc} - V_{ref}, \\ \lim_{t \rightarrow \infty} \Delta V_{dc}(t) &= 0.\end{aligned}\quad (8)$$

Objective 2: To ensure average current sharing among the active DGs in the microgrid, the current-sharing error in each DG must be equal to zero. This is mathematically expressed as

$$\begin{aligned}\Delta I_{o_i}(t) &= k_i I_{o_i}(t) - \frac{1}{N-1} \sum_{j \in N} k_j I_{o_j}(t), \\ \lim_{t \rightarrow \infty} \Delta I_{o_i}(t) &= 0.\end{aligned}\quad (9)$$

It worth noting that *Objective 1* is a global objective, i.e., one DC voltage must be maintained in the microgrid, while *Objective 2* is a consensus objective, i.e., each DG must ensure $\Delta I_{o_i}(t) = \Delta I_{o_j}(t) = \dots = \Delta I_{o_N}(t)$. In the next section, the secondary control is proposed to meet the formulated objectives.

3. Distributed Secondary Control Scheme

In this section, the distributed secondary control scheme to achieve average current sharing and voltage regulation for an islanded DC microgrid is presented.

3.1. Design of Secondary Controller

In the hierarchical control scheme, the secondary controller is implemented to offset any deviation caused by the droop controller in the primary control loop. In the islanded DC microgrid, the proposed secondary control is used to generate the required voltage reference. Based on the control objectives in the previous section, the DC voltage in (6) can be regulated while ensuring average current sharing by using the secondary control, which is expressed as

$$V_{dc}(t) = V_{ref} - (R_{line_i} + R_{d_i}) I_{o_i}(t) + \mathcal{U}_i(t) \quad (10)$$

where \mathcal{U}_i is the adjustment signal from the proposed secondary controller that is designed as

$$\begin{cases} \mathcal{U}_i(t) &= \gamma_i \int e_i(t) dt \\ e_i(t) &= \alpha_i \Delta V_{dc}(t) + \beta_i \Delta I_{o_i}(t) \end{cases} \quad (11)$$

where $e(t)$ is the combined current and voltage deviation; and α , β , and γ are the weights of the proposed secondary control scheme. The magnitudes of these weights affect the response of the secondary control scheme; a large value of α causes a faster convergence of *Objective 1*, while a large value of β leads to the faster convergence of *Objective 2*. To avoid the trade-off between the current-sharing and voltage regulation properties of the secondary controller, γ should be carefully designed. The design/tuning process of γ is usually complex and time-consuming due to the variability of the DC microgrid; therefore, the type-II fuzzy logic scheme is proposed for adaptive selection of γ to ensure both objectives are met simultaneously.

3.2. Basics of Type-II Fuzzy Logic System

The structure of the type-II fuzzy logic system is shown in Figure 2. It has three basic stages: fuzzification—conversion of the crisp input to fuzzy input; inference engine—uses the rule base to determine the fuzzy output; type reduction/defuzzification—reduces and converts the fuzzy output into crisp output [28].

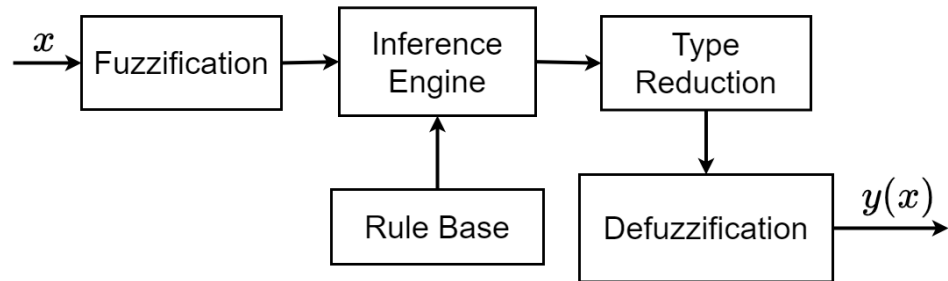


Figure 2. Type-II fuzzy logic system.

The type-II fuzzy set (FS) which is connoted by \tilde{F}_s can be expressed as

$$\tilde{F}_s = \left\{ \left((a, b), \mu_{\tilde{F}_s}(a, b) \right) \mid \forall a \in A, \forall b \in J_a \subseteq [0, 1] \right\}, \tag{12}$$

where $\mu_{\tilde{F}_s}(a, b)$ is a type-II membership function (MF), a , and m are the primary and secondary variables, respectively, in which $0 \leq \mu_{\tilde{F}_s}(a, b) \leq 1$, and J_a is the primary membership of a . When all $\mu_{\tilde{F}_s}(a, b) = 1$, \tilde{F}_s is an interval type-II FS. Due to the uncertainty present in the interval type-II FS, the bounds of uncertainty are defined as the footprint of uncertainty (FOU). The exterior ($\bar{\mu}$) of the FOU is the upper membership function (UMF), and the interior ($\underline{\mu}$) is the lower membership function (LMF) so that

$$\text{FOU}(\tilde{F}_s) = \bigcup_{a \in A} J_a, \tag{13}$$

$$\bar{\mu}_{\tilde{F}_s}(a) \equiv \overline{\text{FOU}}(\tilde{F}_s), \quad \underline{\mu}_{\tilde{F}_s}(a) \equiv \underline{\text{FOU}}(\tilde{F}_s) \quad \forall a \in A. \tag{14}$$

The rule construction for the type-II fuzzy logic is similar to the type-1 fuzzy logic scheme. For a fuzzy system with a K rule, the j^{th} rule can be expressed in the form:

$$R_j : \text{If } a_1 \text{ is } \tilde{F}_{s1,mj} \text{ and } \dots a_i \text{ is } \tilde{F}_{si,j} \dots \text{ and } a_l \text{ is } \tilde{F}_{sl,j}; \quad \text{Then } y \text{ is } \tilde{Y}_j, \tag{15}$$

where $j = (1, 2, \dots, K)$. The type reduction block maps the type-II FS into a type-1 FS by calculating the centroid of the type-II FS associated with each fired rule using

$$Y_{\tilde{F}_s} = 1 / \{y_l, \dots, y_m\}, \tag{16}$$

where y_l and y_m are given as

$$y_l = \frac{\sum_{i=1}^M y_i \bar{\mu}_{\tilde{F}_s}(y_i) + \sum_{i=L+1}^N y_i \underline{\mu}_{\tilde{F}_s}(y_i)}{\sum_{i=1}^L \bar{\mu}_{\tilde{F}_s}(y_i) + \sum_{i=L+1}^N \underline{\mu}_{\tilde{F}_s}(y_i)}, \quad y_m = \frac{\sum_{i=1}^L y_i \underline{\mu}_{\tilde{F}_s}(y_i) + \sum_{i=M+1}^N y_i \bar{\mu}_{\tilde{F}_s}(y_i)}{\sum_{i=1}^M \underline{\mu}_{\tilde{F}_s}(y_i) + \sum_{i=M+1}^N \bar{\mu}_{\tilde{F}_s}(y_i)}. \tag{17}$$

The switching points L and M in (17) are calculated using the Karnik–Mendel (KM) algorithm [29]. The crisp output y of the type-II is computed through defuzzification using

$$y(x) = \frac{1}{2} [y_l + y_m] \tag{18}$$

4. Proposed Type-II Fuzzy-Based Distributed Secondary Control Scheme

In this section, the application of the type-II fuzzy logic scheme to achieve the control objectives of current sharing and voltage regulation is presented.

4.1. Proposed Fuzzy-Based Distributed Secondary Controller

The control framework of the proposed distributed type-II fuzzy secondary control scheme for an islanded DC microgrid is depicted in Figure 3. By convention, the primary control uses the droop method to achieve current sharing for power allocation between multiple DGs in the microgrid. The action of the droop controller leads to a deviation in the DC bus voltage of the microgrid. Thus, it is difficult to simultaneously ensure current sharing and voltage regulation using the primary controller. The secondary controller is employed to solve the limitation of the droop controller. The type-II fuzzy logic scheme is used in the proposed distributed secondary scheme. Each DG has its secondary controller and communicates with other secondary controllers of the other DGs to achieve the distributed property. The inputs to the type-II fuzzy system of DG_i are the voltage deviation (ΔV_{dc}) and the current-sharing error (ΔI_{o_i}). The output of the fuzzy-based secondary controller is the adjustment signal U_i to correct the deviation caused by the primary controller. The mathematical expression of the proposed distributed type-II secondary controller is given as

$$\begin{cases} U_i &= \gamma \int e_i(t) dt \\ e_i(t) &= \alpha \Delta V_i + \beta \Delta I_{o_i} \\ \gamma &= \Omega(\Delta V_{dc}, \Delta I_{o_i}) \end{cases} \quad (19)$$

where $\Omega(\cdot)$ is the proposed type-II fuzzy logic scheme. The membership functions (MFs) of the fuzzy logic system are used for the nonlinear representation of the input–output characteristics of the fuzzy system. Five MFs—positive Large (PL), positive small (NS), an zero (Z), negative small (PS), and negative large (NL)—are selected for the input variables, and three MFs—large (L), medium (M), and small (S)—are selected for the output variable as shown in Figure 4. Using the fuzzy rules provided in Table 1, the If–then rules are designed in the inference engine of the fuzzy system. The output of the type-II fuzzy logic control is multiplied by the integral of the combined error to generate the adjustment signal that meets the control objectives.

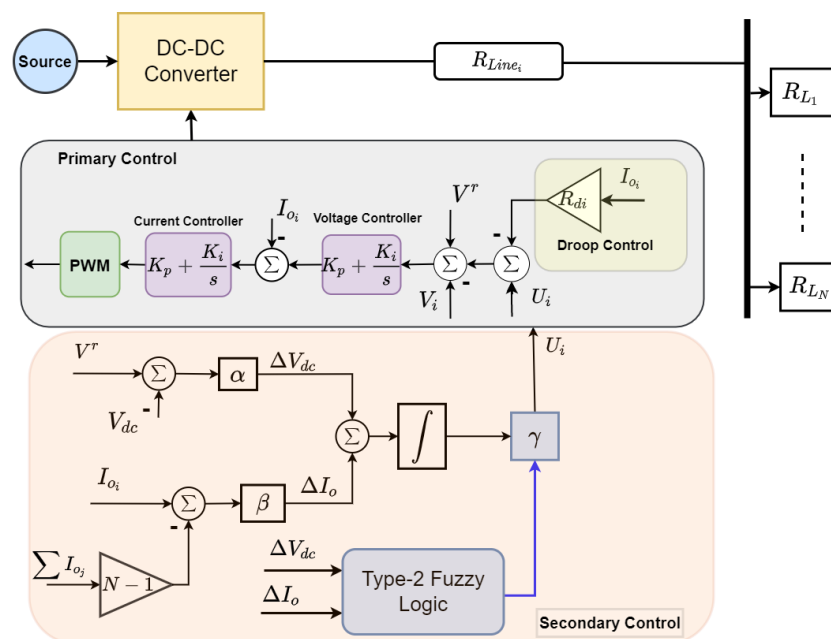


Figure 3. Proposed fuzzy-based distributed secondary control scheme.

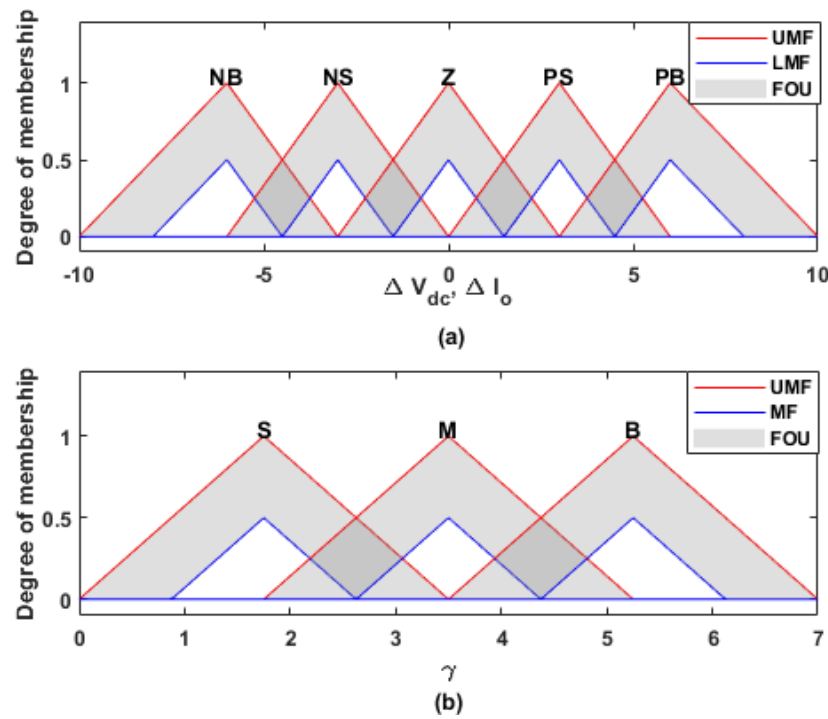


Figure 4. FOU of type-II fuzzy logic membership functions.

Table 1. Table of fuzzy rules.

		ΔV_{dc}				
		PL	PS	Z	NS	NL
ΔI_{o_i}	PL	L	L	M	M	M
	PS	L	L	S	M	M
	Z	L	S	S	S	L
	NS	M	M	S	L	L
	NL	M	M	M	L	L

4.2. Stability Analysis of Proposed Controller

Here, we provide the sufficient condition that guarantees the stable operation of the proposed distributed controller. From (2) and (10)

$$\begin{aligned}
 \dot{I}_{o_i}(t) &= \frac{1}{R_{line,i}} (\dot{V}_i(t) - \dot{V}_{dc}(t)) \\
 &= \frac{1}{R_{line,i}} (\dot{U}_i(t) - (R_{Line_i} + R_{d_i}) \cdot \dot{I}_{o_i}(t) - \dot{V}_{dc}(t)) \\
 &= \frac{1}{\mathbf{R}_i} (\dot{U}_i(t) - \dot{V}_{dc}(t))
 \end{aligned}
 \tag{20}$$

where $\mathbf{R}_i = 2R_{Line_i} + R_{d_i}$. Similarly, from (3), $\sum_{i \in I_s} \dot{I}_{o_i} = \dot{V}_{dc} \sum_{j \in I_L} 1/R_{load_j}$, the current and voltage derivatives of DG_i with respect to the distributed secondary control input derived from (20) is given by

$$\dot{I}_{o_i}(t) = \frac{1}{\mathbf{R}_i} \left(\mathbf{I}_{N \times 1} - \frac{\sum_{i \in I_s} 1/\mathbf{R}_i}{\mathcal{D}} \right) \dot{U}_i(t)
 \tag{21}$$

$$\dot{V}_{dc}(t) = \left(\frac{\sum_{i \in I_s} 1/R_i}{\mathcal{D}} \right) \dot{U}_i(t) \tag{22}$$

where $\mathcal{D} = \sum_{i \in I_s} \frac{1}{R_i} + \sum_{i \in I_L} 1/R_{L_i}$. Denoting $I = [I_{01}, \dots, I_N]^T$, $\mathbf{C} = \text{diag}(c_1, \dots, c_N)$, $\mathcal{U} = [\mathcal{U}_1, \dots, \mathcal{U}_N]^T$, $V = [V_1, \dots, V_N]^T$, and $\mathbf{G} = \text{diag}(1/R_1, \dots, 1/R_N)$, we can represent (8) and (9) as

$$\Delta \dot{V}(t) = \dot{V}_{dc}(t) = \frac{\mathbf{1}_{1 \times N} \mathbf{G}}{\mathcal{D}} \dot{U}(t) \tag{23}$$

$$\Delta I_o(t) = \mathcal{L} \mathbf{C}^{-1} \mathbf{G} \mathbf{Y} \dot{U}(t) \tag{24}$$

where $\Phi = \mathbf{I}_{N \times N} - \mathbf{1}_{1 \times N} \mathbf{1}_{N \times 1} \mathbf{G}$.

Lemma 1. Assuming a symmetric matrix \mathbf{Z} with the property

$$\lambda_{\min} \mathbf{I} \leq \mathbf{Z} \leq \lambda_{\max} \mathbf{I}$$

where \mathbf{I} is an identity matrix of appropriate dimension, and λ_{\min} and λ_{\max} are minimum and maximum eigenvalues of \mathbf{Z} . The matrix \mathbf{Z} that satisfies the Lyapunov equation

$$\zeta^T \mathbf{Z} + \mathbf{Z} \zeta = -\mathbf{Q} \tag{25}$$

where \mathbf{Q} is a positive definite matrix, and $\zeta = \frac{\alpha}{\mathcal{D}} \mathbf{1}_{1 \times N} \mathbf{1}_{N \times 1} \mathbf{G} + \beta \mathcal{L} \Phi$.

Lemma 2. Assuming an undirected graph $\mathcal{L}(\mathcal{G})$ whose objective is to achieve average current sharing given that $\Delta I_o = \mathcal{L} \mathbf{I}$, where $\mathbf{I} = \mathbf{1}_{N \times 1}$ is the solution to $\Delta I_o = 0$.

Theorem 1. The proposed distributed secondary controller in (19) achieves current-sharing and voltage restoration objectives given in (8) and (9), i.e., the combined deviation $\eta(t)$ asymptotically tends to zero if the following condition is met: $\frac{1}{\mathfrak{H}} \lambda_{\min}(\mathbf{P}) < 0$, where λ_{\min} is the minimum eigenvalue of \mathbf{P} .

Proof of Stability. Defining a Lyapunov function

$$\mathcal{V}(t) = e^T(t) \mathbf{Z} e(t) \tag{26}$$

Taking the first derivative of (26)

$$\dot{\mathcal{V}}(t) = \dot{e}^T(t) \mathbf{Z} e(t) + e^T(t) \mathbf{Z} \dot{e}(t) \tag{27}$$

From (19) and (23)–(25), we can derive

$$\dot{e}(t) = \frac{\alpha}{\mathcal{D}} \mathbf{1}_{1 \times N} \mathbf{1}_{N \times 1} \mathbf{G} \dot{U}(t) + \beta \mathcal{L} \Phi \dot{U}(t) \tag{28}$$

Therefore,

$$\begin{aligned} \dot{\mathcal{V}}(t) &= \left(\frac{\alpha}{\mathcal{D}} \mathbf{1}_{1 \times N} \mathbf{1}_{N \times 1} \mathbf{G} \dot{U}(t) + \beta \mathcal{L} \Phi \dot{U}(t) \right)^T \mathbf{Z} \eta(t) \\ &\quad + e^T(t) \mathbf{Z} \left(\frac{\alpha}{\mathcal{D}} \mathbf{1}_{1 \times N} \mathbf{1}_{N \times 1} \mathbf{G} \dot{U}(t) + \beta \mathcal{L} \Phi \dot{U}(t) \right) \end{aligned} \tag{29}$$

which can be simplified as

$$\begin{aligned} \dot{\mathcal{V}}(t) &= \zeta^T \dot{U}(t)^T \mathbf{Z} e(t) + e^T(t) \mathbf{Z} \zeta \dot{U}(t) \\ &= \zeta^T \dot{U}(t)^T \mathbf{Z} \dot{U}(t) + \dot{U}^T(t) \mathbf{Z} \Psi \dot{U}(t) \\ &= \dot{U}(t)^T (\zeta^T \mathbf{Z} + \mathbf{Z} \zeta) \dot{U}(t) \end{aligned} \tag{30}$$

$$\begin{aligned}\dot{V}(t) &= -\frac{1}{\mathbf{1}} \left(\dot{U}(t)^T \mathbf{Q} U(t) \right) \\ \dot{V}(t) &\leq -\frac{1}{\gamma} \lambda_{\min}(\mathbf{Z}) \|U(t)\|^2\end{aligned}\quad (31)$$

If $\lambda_{\min} > 0$, then $\dot{V}(t) < 0$. Therefore, from Lemma 1, the sufficient condition for the distributed secondary controller to be asymptotically stable requires that the coefficient γ of the proposed distributed secondary controller is designed to be positive. \square

5. Simulation and Discussion

In this section, the performance of the proposed type-II fuzzy-based secondary control scheme for the islanded DC microgrid is investigated. The DC microgrid has four DGs ($N = 4$), each with its primary and secondary controls. The primary control operates using the decentralized feature, while the proposed secondary control adopts the distributed feature using a communication network. The DC microgrid is developed in MATLAB/Simulink and experimentally validated in real-time using the OPAL-RT simulator. The parameters of the microgrid are given in Table 2 [30].

Table 2. DC microgrid Parameters.

Parameter	Symbol	Value
Rated Bus Voltage	V_{dc}	48 V
Source Voltage	V_s	100 V
Sampling Frequency	f_s	10 kHz
Filter Inductance and Capacitance	L, C	1 mH, 235 μ F
Line Resistance	$R_{line,i} \ i = 1, 4$	0.5 Ω , 0.2 Ω , 0.6 Ω , 0.3 Ω
Total Load	$R_{Load_j} \ j = 1, 3$	12 Ω
Primary and Secondary Controls		
Inner Current Controller	K_p, K_i	0.05, 148
Inner Voltage Control	K_p, K_i	0.248, 36
Droop resistance	$R_{d_i} \ i = 1, 4$	1 Ω
Voltage Deviation weights	α	1.25
Current Deviation weights	β	7.5

5.1. Test for Distributed Average Current Sharing and Voltage Regulation

In this scenario, the effectiveness of the proposed secondary controller to achieve the control objectives is investigated. Two information-sharing topologies are tested as shown in Figure 5. Topology A represents fully distributed communication; Topology B represents nearest neighbour distributed communication. It is important to mention that the communication is bidirectional, i.e., $a_{ij} = a_{ji}$. The total simulation time is 10 s. In Stage I, $t \in [0 - 2)$, only the primary controller in each DG is active. In Stage II, $t \in (2 - 10]$, the proposed secondary controllers in all the DGs are activated. The results of this scenario are shown in Figures 6–9. In Figures 6a and 7a, it is observed that the DC bus voltage (38 V) of the microgrid in Stage I is less than the rated DC bus voltage (48 V). This is due to the voltage deviation caused by the droop resistance and line impedance as given in (6). A

similar result is observed in the output voltages of the DGs as shown in Figures 6b and 7b. In Stage II, when the proposed fuzzy-based secondary controller is activated in the DGs, it is observed that the DC bus voltage of the microgrid rises to the rated value, and the output voltage of each DG rises above the rated value. The voltage rise above the rated value is a result of the line impedance between each DG and the DC bus that is governed by $V_i = V_{dc} + R_{line_i} I_{o_i}$. The output current of each DG depicted in Figures 8 and 9 show that DGs are unable to achieve average or consensus current sharing in Stage I. However, in Stage II, the objective of current sharing is achieved within 2 s of activation when all DGs have the same output current of 9.6 A in the microgrid.

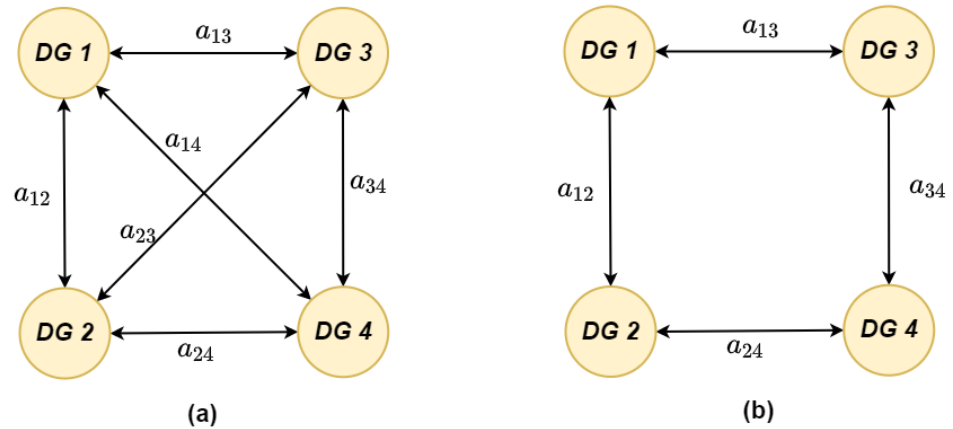


Figure 5. (a) Topology A; (b) Topology B.

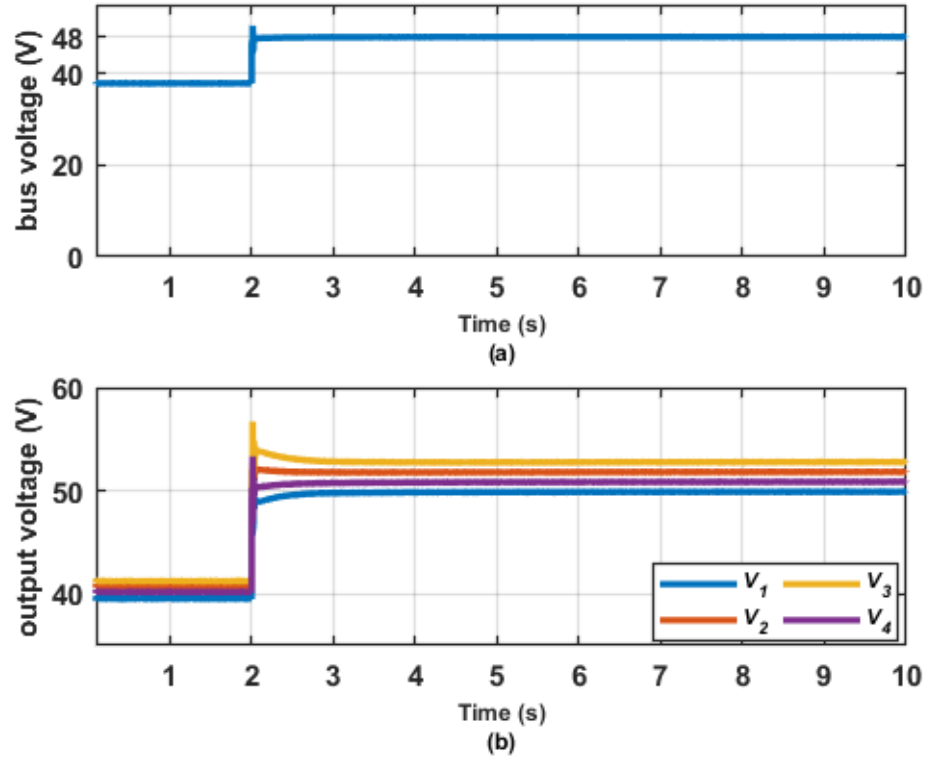


Figure 6. (a) DC bus voltage; (b) output voltage of DGs using Topology A.

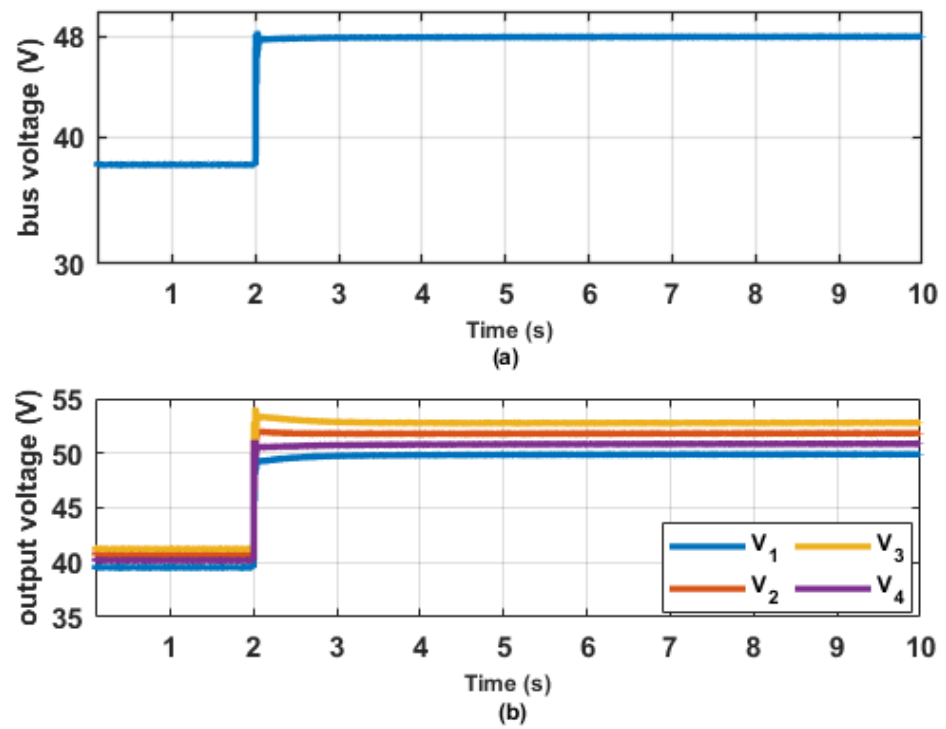


Figure 7. (a) DC bus voltage; (b) output voltage of DGs using Topology B.

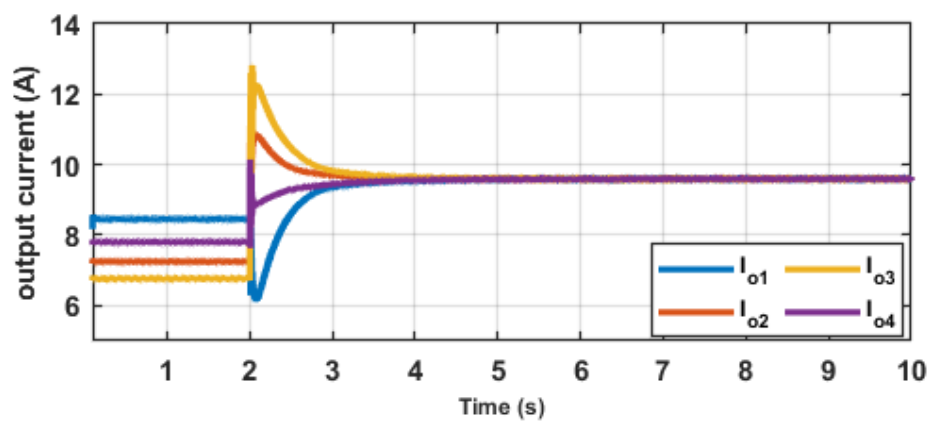


Figure 8. Output currents of DGs using Topology A.

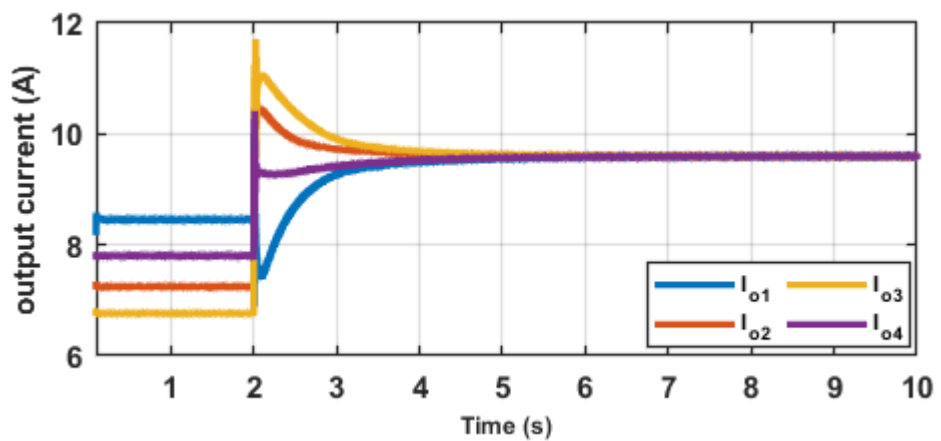


Figure 9. Output currents of DGs using Topology B.

By critically inspecting the voltage and current responses in this scenario, it is observed that the communication network using Topology A is characterized by higher magnitudes of transients when the secondary controller is activated compared to Topology B. Therefore, it can be surmised that the proposed fuzzy-based secondary control scheme can ensure current-sharing and voltage regulation objectives with the distributed property.

Table 3 presents the superiority of the proposed type-II fuzzy logic scheme for the defined control objectives compared to the traditional secondary control scheme with fixed gains [31]; it is seen that the traditional control scheme takes 2 s and 2.8 s to achieve the voltage regulation and current-sharing objectives, respectively. However, the proposed scheme achieves the voltage regulation objective in less than 0.1 s, and the current-sharing objective is approximately 2 s.

Table 3. Proposed scheme with other control schemes.

Control Objective	Traditional Secondary Control	Proposed Type-II Fuzzy Logic Control
Objective 1	2 s	≤ 0.1 s
Objective 2	2.8 s	2 s

5.2. Performance of Proposed Controller During Load Change

In this scenario, the response of the proposed fuzzy-based distributed secondary controller during a change in load/demand is evaluated. Using the sparse communication network (Topology B) in the previous scenario, the simulation lasts for 25 s. In Stage I, $t \in [0 - 2)$, only the primary controllers in the DGs are active. In Stage II, $t \in (2 - 8)$, the proposed secondary controller in each DG is activated. In Stage III, $t \in (8 - 15)$, an additional load is connected at the DC bus. In Stage IV, $t \in (15 - 25]$, the load is disconnected from the microgrid. The results are presented in Figures 10–12. The results for Stages I and II are similar to the previous scenario. The DC bus voltage and output voltages are shown in Figure 10. In Stage III, when the load is connected to the DC bus, there is a short voltage transient at the DC bus that is quickly eliminated to restore the bus voltage to 48 V. Likewise, when the load is disconnected from the microgrid in Stage IV, the DC bus voltage overshoot is dampened and maintained at the rated value to keep the system in the stable mode. It is generally observed that the output voltages of the DGs are higher than the rated voltage due to the effect of the line impedances between the DGs and the DC bus as previously described. The output current plots shown in Figure 11 reveal that the addition of the load increases the current generated by each DG, while the disconnection of the load causes a drop in the output. In Stages III and IV, the microgrid can ensure average current sharing among the DGs in both stages based on the control objectives of the proposed secondary controller. The output adjustment signal, U , of the proposed fuzzy-based secondary controller for each DG is shown in Figure 12. The adjustment signal eliminates the deviation caused by the action of the droop control as given in (11).

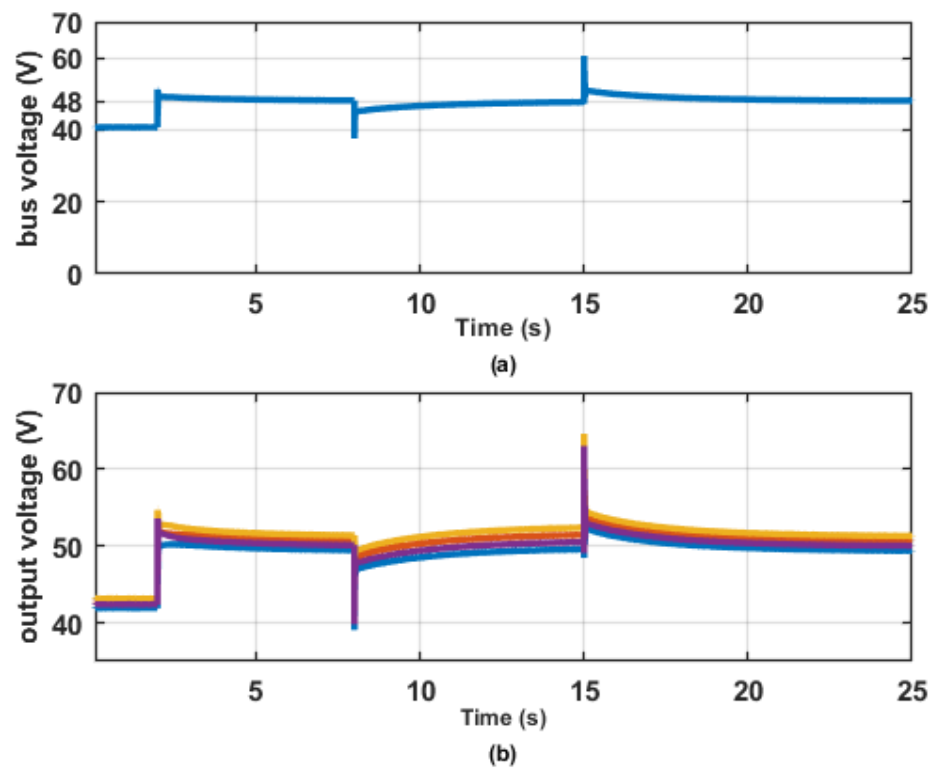


Figure 10. (a) DC bus voltage; (b) output voltage of DGs.

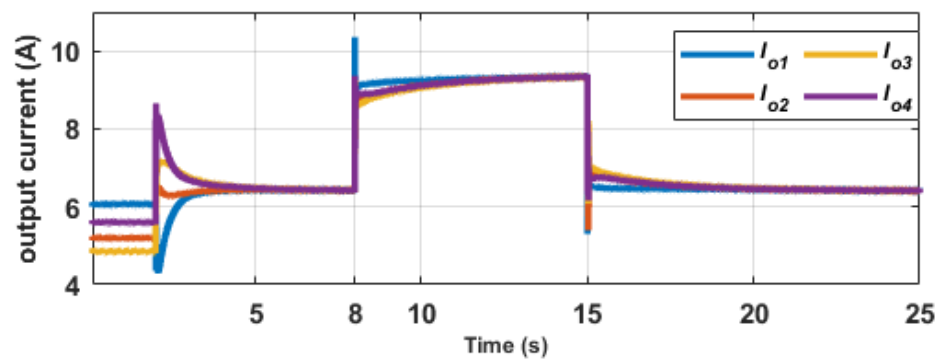


Figure 11. Output currents of DGs.

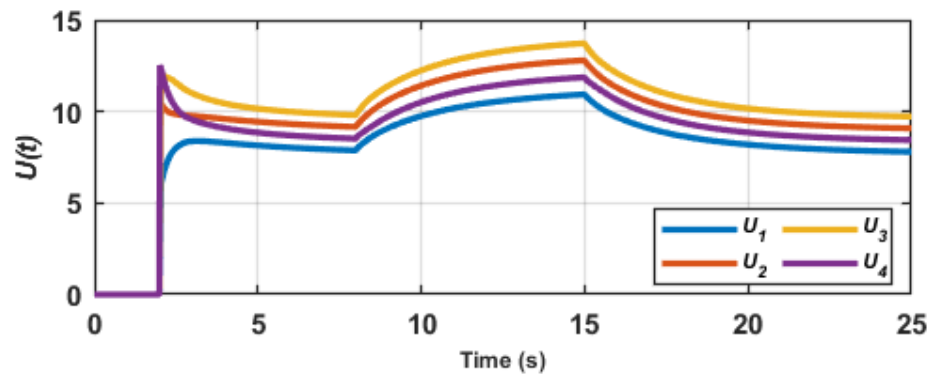


Figure 12. Output of proposed distributed secondary controller.

5.3. Performance Under Communication Time Delay

In this scenario, a practical case of communication time delay among the proposed secondary controllers in each DG is considered to elucidate the robustness of the proposed

control scheme. We consider various time delays between the secondary controllers using Topology A; $a_{12} = 100$ ms, $a_{23} = 250$ ms, and $a_{34} = 150$ ms. The total simulation time is 10 s. In Stage I, $t \in [0 - 5)$, the primary and proposed secondary controllers in the DGs are active. In Stage II, $t \in (5 - 10]$, a load is switched on at the DC bus. The results of this scenario are shown in Figures 13 and 14. From Figure 13a, it is observed that the output currents of the DGs have different starting times in Stage I due to the different time delays in the communication network of the microgrid, but the proposed fuzzy-based secondary controller can ensure consensus current sharing in < 1 s of operation. Similarly, in Stage II, when an additional load is switched on, the output current from each DG changes to meet the additional demand. The effect of time delay is noticed in the response of the output currents. However, the total current demand is equally shared among the DGs. The response of the DC bus voltage in this scenario is seen in Figure 13b. It is observed that the effect of the time delay is not conspicuous when compared to the output current response. This is because the voltage regulation objective is a global objective that requires control action from a minimum of one active DG, while the current-sharing objective is a consensus objective that requires the control action of all active DGs in the microgrid. The control action (adjustment signal) of the proposed distributed secondary controllers in the microgrid is shown in Figure 14. It is observed that each secondary controller reacts to meet the control objectives to ensure that the DC voltage is maintained at 48 V, and the load current is equally shared among the DGs.

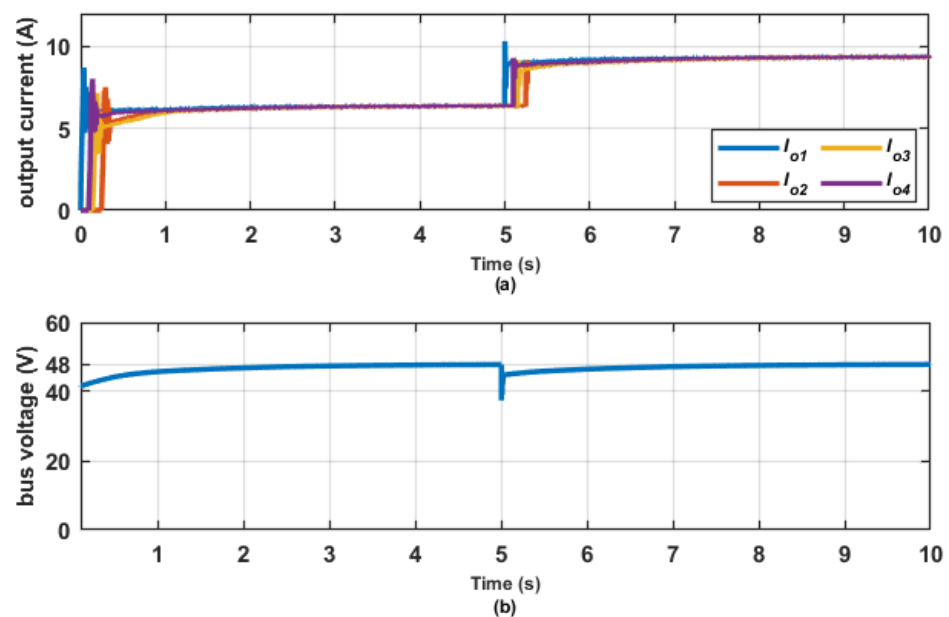


Figure 13. (a) Output current of DGs; (b) DC bus voltage under different communication time delays.

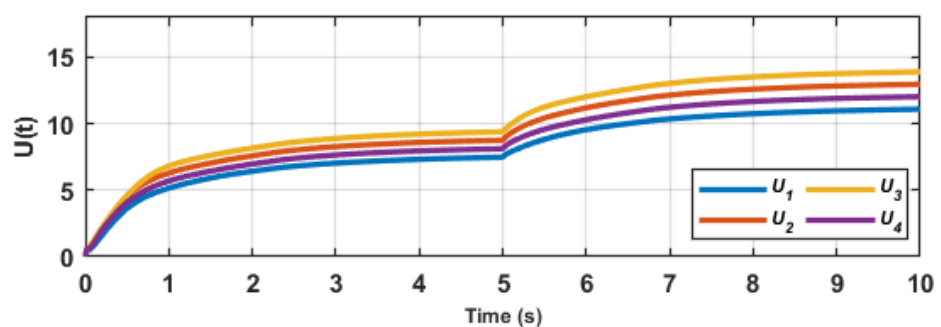


Figure 14. Output of proposed distributed secondary controller under different communication time delays.

6. Conclusions

This paper presents a new advanced distributed secondary control scheme for an islanded DC microgrid with multiple DGs. The proposed secondary control scheme is employed to compensate for the voltage deviation caused by the droop control and simultaneously ensure average current sharing amongst the DG. The type-II fuzzy logic scheme is employed to improve the performance of the proposed secondary controller. The inputs to the type-II fuzzy logic are the voltage deviation and current-sharing error. Its output is the secondary control parameter that modifies the voltage and current-sharing weights. Each DG has its type-II fuzzy-based secondary controller and communicates with other controllers to achieve the distributed approach. The Lyapunov stability method showed the required condition to ensure the stable operation of the proposed controller in the microgrid. The simulation results show that the proposed fuzzy-based secondary control scheme can efficiently ensure accurate voltage regulation and current sharing under plug-and-play, load change, and communication time delay scenarios. The proposed type-II fuzzy scheme can be applied to real-world DC microgrids to seamlessly solve the current sharing and voltage regulation issues. Further work may include the addition of nonlinear loads, different converter topologies, and the adoption of hybrid DGs.

Author Contributions: Conceptualization, A.A. and E.B.; methodology, A.A., E.B. and O.E.O.; software, A.A. and E.B.; validation, E.B., O.E.O. and I.E.D.; formal analysis, A.A. and E.B.; writing—original draft preparation, A.A. and E.B.; writing—review and editing, E.B., O.E.O. and I.E.D.; supervision, I.E.D.; project administration, I.E.D.; funding acquisition, I.E.D. All authors have read and agreed to the published version of the manuscript.

Funding: This research received no external funding and The APC was funded by DUT Smart Grid Research and DUT Directorate for Research and Postgraduate Supports.

Acknowledgments: The authors acknowledge the facility support from the Smart Grid Research Center at the Durban University of Technology.

Conflicts of Interest: The authors declare no conflict of interest.

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